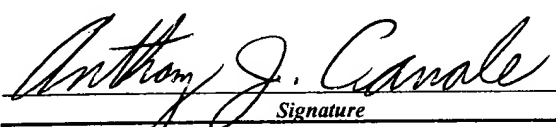
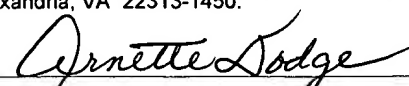
 TRANSMITTAL OF FORMAL DRAWINGS				Docket No. JP920000229US1	
In Re Application Of: Fujio et al					
Application No. 09/682,131	Filing Date 07/25/2001	Confirmation No. 2739	Examiner Chris C. Chu	Customer No. 24241	Group Art Unit 2815
Invention: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE WITH EMI PREVENTION STRUCTURE					
<p>Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450</p> <p>Transmitted herewith are:</p> <p>7 sheets of formal drawing(s) for this application.</p> <p><input checked="" type="checkbox"/> Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).</p>					
 Signature			Dated: 09/01/2004		
Anthony J. Canale, Reg. No. 51,526 IBM Corporation Intellectual Property Law Dept., 972E 1000 River Street Essex Junction, VT 05452			<div>I certify that this document and attached formal drawings are being deposited on <u>Sept 1, 2004</u> with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.</div> <div> Signature of Person Mailing Correspondence</div> <div>ARNETTE DODGE Typed or Printed Name of Person Mailing Correspondence</div>		